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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/735,436	
	Filing Date	12-12-2003	
	First Named Inventor	Heinrich Sussner	
	Group Art Unit	2811	
	Examiner Name		
Total Number of Pages in This Submission	41	Attorney Docket Number	SUSS-001

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Heinrich Sussner, Applicant
Signature	<i>H. Sussner</i>
Date	04/15/04

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Substitute for form 1449A/PTO

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Sheet	1	of	2
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**Complete if Known**

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<b>First Named Inventor</b>	Heinrich Sussner
<b>Art Unit</b>	
<b>Examiner Name</b>	
<b>Attorney Docket Number</b>	SUSS-001

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		REOHR et al., "MEMORIES OF TOMORROW", IEEE CIRCUITS & DEVICES MAGAZINE, SEPTEMBER 2002, pp. 17-27	
		DESIKAN et al., "On-chip MRAM as a High-Bandwidth, Low-Latency Replacement for DRAM Physical Memories", Department of Computer Sciences Tech Report TR-02-47, The University of Texas at Austin, September 27, 2002	
		KATTI, "Current-in-plane pseudo-spin-valve device performance for giant magnetoresistive random access memory applications (invited)", JOURNAL OF APPLIED PHYSICS, VOLUME 91, NUMBER 10, 15 MAY 2002, PP. 7245-7250	
		KATINE et al., "Current-induced realignment of magnetic domains in nanostructured Cu/Co multilayer pillars", Appl. Phys. Lett., Vol. 76, No. 3, 17 January 2000, PP. 354-356	

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